Bachelor of Science (T.Y.B.Sc.) (Part-III) Sixth Semester

B.Sc. 4517 - Electronics Paper-I (Compulsory) Microprocessor, Interfacing and Microcontrollers

P. Pages: 1 Time: Three Hours			GUG/W/1 * 1 0 5 7 * Max. M		
	Note		. Draw a diagram wherever necessary.		
1.	Either		t is dehouncing of a switch? Evaloin Drow a suitable diagram of 4v4 matrix	5+5	
			is debouncing of a switch? Explain. Draw a suitable diagram of 4x4 matrix pard interfacing and explain.	3+3	
			OR		
	b)	_	ain interfacing of LED with 8085 microprocessor. Draw suitable diagram for facing SSD and explain its working.	5+5	
2.	Either				
	a)	Exp	ain measurement of phase and frequency using microprocessor.	5+5	
	1. \	XX 71	OR	2 . 4 . 4	
•	b)		t is delay subroutine? Explain delay subroutine using one register and register pair.	2+4+4	
3.	Eithe a)		w block diagram of 8086 microprocessor and explain function of each block.	10	
	α)	Dia	OR	10	
	b)		t is addressing mode? Explain any three addressing modes of 8086 μp. with ble example.	1+9	
4.	Eithe	Either			
	a)	Draw block diagram of 8051 microcontroller and explain function of each block. State the common features of 8051 microcontroller.			
			OR		
	b)	State	the meaning of following instructions in 8051 microcontroller.	10	
		i) ii)	ADD A, R ₂ MOV A, #25H		
		iii) iv)	INC R. MOV R3, #3		
		v)	ORG OH		
5.		a)	Explain DIP with interfacing with microprocessor.	$2^{1/2}x4=$	
		b)	Explain microprocessor based traffic control.	10	
		c)	What are assembler and directives? Explain.		
		d)	Explain common features of 8096 microcontroller.		
