B.E.(with Credits)-Regular-Semester 2012-Computer Technology Sem III CT 302 - Computer Architecture & Organization

P. Pages: 1 Time: Three Hours			* 3 6 0 3 *	
	Note	2. Illustrate your answers	s wherever necessary with the help of ne en to neatness and adequate dimensions.	
1.	a)	Explain 3-bus structure with th	e help of diagram.	8
	b)	Write a note on Distributed computing.		8
			OR	
2.	a)	Explain Addressing modes.		8
	b)	What are the operations of mai	n memory explain.	8
3.	a)	Write a note on 'Array processo	ors'	8
	b)	Compare lossely coupled and to		8
	,	1 7 1	OR	
4.	a)	Explain execution of a complete instruction, Write and explain control sequence for execution of the instruction ADD(R3), R1.		sequence for 8
	b)	Write short note on "I/O Device	es".	8
5.	a)	Explain the types of microinstructions available.		8
	b)	What is Bit slicing? Explain wi	ith example.	8
			OR	
6.	a)	Differentiate between Hardwire	ed & Microprogrammed computer.	8
	b)		ed micro-instruction. Explain why group	ing of control 8
7.	a)	Explain with an example how t	to multiply two unsigned binary numbers	s. 8
	b)	Write a short note on carry lool		8
		·	OR	
8.	a)	Write a note on "Arithmatic op	perations on floating point numbers".	8
	b)	=	multiplication & apply it to multiply (-13	
9.	a)	Explain memory Inter leaving i	in detail.	8
	b)	What is cache memory? Explai		8
	,	, 1	OR	
10.	a)	Explain virtual memory Addres	ss translation.	8
	b)	<u> </u>	gement? Explain memory management t	