NOTE: 1. Figure should be Neat and labeled.

- 2. All Questions are compulsory.
- 3. Right side indicates marks.

Q.1 Attempt the fe		[40marks]
1. Inis fla	ag is used to indicate parity of the result i.e. number	ers of one's. If the result
	bers of one's this flag is set to 1.	
a) S	b) Z	
c) AC	d) P	
2. Which interrupt	has the highest priority?	n Irsus ad he grorg A. A
a) TRAP	b) RST7.5	
c) RST6.5	d) RST5.5	
3. This si	gnal indicates that another master is requesting the	e use of the address and
data buses.	and the state of t	e ase of the address and
a) HOLD	b) HLDA	
c) RESET	d) READY	
4. The data bus of N	Microprocessor is	
a) Unidirectional	b) Bidirectional	
	s well as bidirectional d) None of these	
	d) Notice of these	
5. LDA is	instruction.	
a) arithmetic	b) logical	
c) data transfer	d) branching	
6. If A=56H, B=82	H after execution of ANA B, content of A=	
a) 02H	b) 20H	the second
c) 04H	d) 40H	
7. XCHG instructio	n of 8085 exchange the content of	
a) BC & DE	b) BC & HL	
c) DE & HL	d) A & HL	
8. Which instruction	n is required to rotate the content of accumulator o	ne bit right along with
carry?		
a) RRC	b) RLC	
c) RAR	d) RAL	
9. The process of st	oring the data in the stack is called the sta	ack
a) Pulling into	b) Pulling out	The tribute was a mand (in
c) Pushing into	d) Pushing out	
10. In case of nested	subroutines the return addresses are stored in	
a) System Heap	b) special memory buffer	•
c) processor stack		

11. The books area) Queue	ranged one on the other on a table is an ex b) Stack	cample of
c) FIFO	d) Random Access	sa trel' ad blands capital celling
12. The stack is u		gena e u suroitenet lle A
a) storing the reg	rister status of the processor	b) temporary storage of data
c) storing conten	ts of registers temporarily inside the CPU	d) all of the mentioned
	number 80 is represented in its BCD form	1 as
	b) 11000000	
c) 10000001	d) 0110000	
14. A group of b	its used to represent a symbol is called a _	
a) code	b) byte	
c) nibble	d) bit	
15. The weights	used in Binary coded decimal code are	
a) 8,4,2,1	b) 8,6,4,1	REF CONTROLL BRIDGE
c) 6,4,2,1	d) 8,6,2,1	
16. Write the de	cimal equivalent for (110001) _{BCD} is	THESE THESE
a) 31	b) 21	all and the second seco
c) 11	d) 22	
17. RISC proces	ssor's first pipeline stage is	
a) Instruction fer	tch b) Instruction read	
c) Instruction sto	ored d) Instruction load	
18. In a pipeline	d processor, a pipeline hasends.	Attenued (b) tellanest used (
a) one	b) two	La Allena marenaren erapearen era
c) three	d) four	1000 10
19. The CMPXO	CHG8B instruction is an extension of	instruction.
a) CMPXCHG	b) CMAXCHG	
c) CMXCHG	d) CPXCHG	
20. APIC stands	s for	
	ogrammable Instruction Controller	
b) Array Progra	mmable Interrupt Controller	
	mmable Instruction Controller	
d) Advanced Pr	ogrammable Interrupt Controller	
Q.2 Attempt th	ne following.	
A) Answer any		[04marks]
1) Explain the f	functions of following units of 8085:	
	ction Register ii) Accumulator	
	ing and control iv) Temp register	
2) Write a short	note on Flag Register.	

B) Answer any ONE. [03marks] 1) Explain T-state, Machine cycle and Instruction Cycle. 2) Explain the types of computers. Q.3 Attempt the following. A) Answer any ONE. [04marks] 1) Draw and explain the timing diagram for execution of OUT instruction. 2) Draw and explain the concept of Input Interfacing. B) Answer any ONE. [03marks] 1) Explain the working of following instructions: a. DAA b. CMC c. STAX rp 2) Explain the working of following instructions: a. ADI 8-bit data b. SBB M c. CMP r Q.4 Attempt the following. A) Answer any ONE. [04marks] 1) Specify the contents of memory location XX70H to XX73H after execution of the following instruction LXIH, XX70H MVI B, 05H MVI A, 01H STORE: MOV M, A INR A INX H DCR B JNZ STORE HLT 2) Draw and explain the concept of Nesting. B) Answer any ONE. [03marks] 1) List down the common source of errors. 2) Write a 8085 microprocessor program in assembly language to find square of a number stored at memory location C200 H. Store the LSB of result at memory location C300H and MSB of result at memory location C301H. Q.5 Attempt the following. A) Answer any ONE. [04marks] 1) Draw and explain DMA. 2) Explain 8085 vectored interrupts. B) Answer any ONE. [03marks] 1) Write a short note on system hardware and storage memory. 2) What is OS? Explain the types of OS. Q.6 Attempt the following. A) Answer any ONE. [04marks] 1) Draw and explain block diagram of Pentium Pro. 2) Draw and explain the Pentium Pro dispatch and execution unit. B) Answer any ONE. [03marks] 1) What is Pipeline in processor? Explain. 2) Compare Pentium and Pentium Pro Processor.